Welcome
Sujata Tibrewala
Network Developer Evangelist
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What is it?

A DPDK community event

“Creative Commons: en:Flickr User HayathSource:Flickr URL: http://www.flickr.com/photos/hayath/278862366/”
What is a community?
What is a community?

“A community is a small or large social unit (a group of people) who have something in common, such as norms, religion, values, or identity.” Wikipedia

Wikipedians 2012 ➔
How do you evaluate an Open source community

- Contributions
- Releases
- User Community
- Eco system
- Longevity
Open Source Stats

A fully open source software project with a strong development community:

- BSD licensed
- Website: http://dpdk.org; Git: http://dpdk.org/browse/dpdk/

DPDK Release Stats

- Total Contributors
- Total Commits

Major Contributors

- IBM
- CISCO
- NXP
- Mellanox Technologies
- Linaro
- Other names and brands may be claimed as the property of others.
Multi-Architecture/Multi-Vendor Support

**CPU Architectures**
- POWER 8
- TILE-Gx
- ARM v7/v8
- Enhanced ARM Support
  - Cavium
  - NXP
  - RehiveTech
- Event API
  - NXP
- SoC Enhancements
- ARMv8 Crypto
- OcteonTX
- Non-Intel crypto.

**Poll Mode Drivers**
- ENIC
- CISCO
- BNX2X
- MLX4/MLX5
- ThunderX PMD
- BNX
- NFP
- MLX4/MLX5
- ThunderX PMD
- BNXT
- SoC Enhancements
- NXP
- ARMv8 Crypto
- OcteonTX
- LiquidIO

**2014**
First non-IA contributions.

**2015**
Non-Intel NIC support.

**2016**
Significant ARM vendor engagement.

**2017**
SoC enhancements.
Non-Intel crypto.
Linux Foundation Now Hosting DPDK

• Industry support: hardware, software vendors, multiple industry verticals

• 8 founding Gold member supporters, 5 Silver members
First ever DPDK summit in India

Learn
Get Help
Belong
Connect
Give Help

DPDK Summit Attendees (Worldwide)

- Mini-Summit @OPNFV
- EU Summit
- PRC Summit
- USA Summit

2014 2015 2016
Why are you here?

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# Agenda, Day 1

<table>
<thead>
<tr>
<th>Time</th>
<th>Schedule</th>
<th>Speaker</th>
<th>Company</th>
</tr>
</thead>
<tbody>
<tr>
<td>8:00 am - 8:45 am</td>
<td>Registration</td>
<td>Sujata Tibrewala</td>
<td>Intel</td>
</tr>
<tr>
<td>8:45 am - 9:00 am</td>
<td>Introductions, Welcome and Agenda for the day</td>
<td>Kannan Babu Ramia, Deepak K Jain.</td>
<td>Intel</td>
</tr>
<tr>
<td>9:00 am - 9:45 am</td>
<td>DPDK Architecture and Roadmap</td>
<td>Hemant Agarwal, Shreyansh Jain</td>
<td>NXP</td>
</tr>
<tr>
<td>9:45 am - 10:05 am</td>
<td>Supporting SoC devices in DPDK - Status Update</td>
<td>Vamsi Attunuru</td>
<td>Cavium</td>
</tr>
<tr>
<td>10:05 am - 10:30 am</td>
<td>DPDK on an Intelligent NIC</td>
<td>Jingjing Wu, Helin Zhang</td>
<td>Intel</td>
</tr>
<tr>
<td>10:30 am - 10:55 am</td>
<td>Migrating from 10G to 25G</td>
<td>Muthurajan JayaKumar</td>
<td>Intel</td>
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<tr>
<td>10:55 am - 11:00 am</td>
<td>DPDK CookBook</td>
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<tr>
<td>11:00 am - 11:15 am</td>
<td>Tea/Coffee Break</td>
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<tr>
<td>11:15 am - 11:45am</td>
<td>Implementation of Flow-Based QoS Mechanism with OVS and DPDK</td>
<td>Karuppusamy M</td>
<td>Wipro</td>
</tr>
<tr>
<td>11:45 am - 12:00 pm</td>
<td>Fast Path Programming</td>
<td>Rajaraman Subramoniam, Praveen Desu</td>
<td>Happiest Mind</td>
</tr>
<tr>
<td>12:00 pm - 12:30 pm</td>
<td>Dataplane for Subscriber Gateways</td>
<td>Natarajan Venkataraman</td>
<td>Ericsson</td>
</tr>
<tr>
<td>12:30 pm - 2:00 pm</td>
<td>Lunch Break and Demo Zone Visits</td>
<td>Red Hat, Intel, TCS, Wipro, Avaya</td>
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</tbody>
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<tr>
<td>2:00 pm – 2:30 pm</td>
<td>Sample VNF in OPNFV</td>
<td>Ramia Kannan Babu</td>
<td>Intel</td>
</tr>
<tr>
<td>2:30 pm – 3:00 pm</td>
<td>Fast Data IO / Vector Packet Processor: Architecture overview</td>
<td>Shwetha Bhandari</td>
<td>Cisco</td>
</tr>
<tr>
<td>3:00 pm – 3:30 pm</td>
<td>Transport Layer Development Kit (TLDK)</td>
<td>Mohammad Abdul Awal</td>
<td>Intel</td>
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<tr>
<td>3:30 pm – 3:45 pm</td>
<td>Tea/Coffee Break</td>
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<tr>
<td>3:45 pm – 4:15 pm</td>
<td>SFC with OVS-DPDK and FD.io-DPDK</td>
<td>Prasad Gorja</td>
<td>NXP</td>
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<tr>
<td>4:15 pm – 4:30 pm</td>
<td>DPDK Automation in Red Hat OpenStack Platform</td>
<td>Saravanan KR</td>
<td>Red Hat</td>
</tr>
<tr>
<td>4:30 pm – 5:00 pm</td>
<td>Packet Steering for Multicore Virtual Network Applications over DPDK</td>
<td>Priyanka Naik, Mitali Yadav</td>
<td>IT Mumbai</td>
</tr>
<tr>
<td>5:00 pm – 5:30 pm</td>
<td>Cryptodev API</td>
<td>Deepak K Jain</td>
<td>Intel</td>
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<tr>
<td>5:30 pm – 7:30 pm</td>
<td>Evening Reception</td>
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## Agenda, Day 2

<table>
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<td>Sujata Tibrewala</td>
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<td>9:00 am - 10:30 am</td>
<td>Building your own DPDK app from scratch</td>
<td>Yigit Ferruh</td>
</tr>
<tr>
<td>10:30 am - 10:45 am</td>
<td>Tea/Coffee Break</td>
<td></td>
</tr>
<tr>
<td>10:45 am - 11:15 am</td>
<td>Building your own DPDK app from scratch (Contd.)</td>
<td>Yigit Ferruh</td>
</tr>
<tr>
<td>11:15 am - 12:15 pm</td>
<td>DPDK IP pipeline hands on lab (part 1)</td>
<td>Jayakumar Muthurajan</td>
</tr>
<tr>
<td>12:15 pm - 12:45 pm</td>
<td>Lunch Break</td>
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</tr>
<tr>
<td>12:45 pm - 2:15 pm</td>
<td>DPDK IP pipeline hands on lab (part 1)</td>
<td>Jayakumar Muthurajan</td>
</tr>
<tr>
<td>2:15 pm - 2:30 pm</td>
<td>Networking Break</td>
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<tr>
<td>2:30 pm - 3:00 pm</td>
<td>Idea Roulette</td>
<td>Sujata Tibrewala</td>
</tr>
<tr>
<td>3:00 pm - 3:30 pm</td>
<td>How to build an optimized VNF using DPDK</td>
<td>Jayakumar Muthurajan, Jyoti Anand B, Rudramuni, Vishwesh M, S Deepak</td>
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<tr>
<td>3:45 pm - 5:45 pm</td>
<td>How to build an optimized VNF using DPDK (Contd.)</td>
<td>Jayakumar Muthurajan, Jyoti Anand B, Rudramuni, Vishwesh M, S Deepak</td>
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<tr>
<td>5:45 pm - 6:00 pm</td>
<td>Wrap up and idea Roulette prizes</td>
<td>Sujata Tibrewala</td>
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THANK YOU