TLDK overview

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Transport Layer Development Kit (TLDK)

Project web-site: https://wiki.fd.io/view/TLDK

The scope of the project:

- implement a set of libraries for L4 protocol processing (UDP, TCP etc.) for both IPv4 and IPv6.
- create VPP graph nodes, plugins, etc. using those libraries to implement a host stack.
- mechanisms (netlink agents, packaging, etc.) necessary to make the resulting host stack easily usable by existing non-vpp aware software.
TLDK libraries

- The goal is – a lightweight, high performance and easily adaptable implementation for L4(UDP, TCP etc.) protocol processing.

- Built on top of DPDK.
  - Use DPDK API/features across the libraries.
  - Follow DPDK concepts (process packets in bulks, non blocking API, etc).

- The provided API is not compatible with BSD socket API.
  - Though keep similar semantics (whenever possible).

- Not a complete ‘host’ stack.
Current status

- **libtle_udp** - implementation of the UDP datagram processing.
  - Operates over both IPv4 and IPv6 packets.

- **udpfwd** - sample app to demonstrate and test libtle_udp usage.
  - can do simple send/recv or both over opened udp streams.
  - ability to do UDP datagram forwarding between different streams ("UDP proxy").
  - reassemble/fragment IP packets (based on DPDK librte_ip_frag).
In Development

- TCP processing implementation.
  - libtle_tcp.
  - sample application.

- libtle_udp/udpfwd enhancements.
  - Extra features (RSS/FD HW offloads, etc).
Each TLDK context operates independently.

API can be logically divided into:
- Back-End (BE):
  - Config API (dev add/remove).
  - PKT IO (RX/TX bulk).
- Front-End (FE):
  - stream control and IO (open(), close(), listen(), recv(), send(), etc.).

BE API is not thread-safe.
FE API is thread-safe.
Possible deployment scenarios

1. one TLDK ctx, BE and FE on the same core

2. one TLDK ctx, BE and FE on different cores

3. two TLDK ctxs, for each BE and FE on the same core

4. two TLDK ctxs, BE and FE on different cores
UDPFWD peak performance numbers (echo mode, ipv4/udp)

**System Configuration**

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<th>Hardware</th>
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<td>CPU</td>
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<tr>
<td>Cores per Socket</td>
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<tr>
<td>LL CACHE</td>
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<tr>
<td>MEMORY</td>
<td>DDR3 1600 MHz, 2X4GB (total 8GB), 2 Channel per Socket</td>
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<td>BIOS</td>
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<tr>
<td>Software</td>
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<td>Kernel version</td>
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<tr>
<td>Other</td>
<td>DPDK 16.07</td>
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UDPFWD peak performance numbers (echo mode, ipv4/udp)

MULTIPLE STREAMS, 64B PKT

1) fe+be@1core, 1HW queue
2) fe and be@separate cores, 1HW queue
3) 2x(fe+be@1core), 2HW queues

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