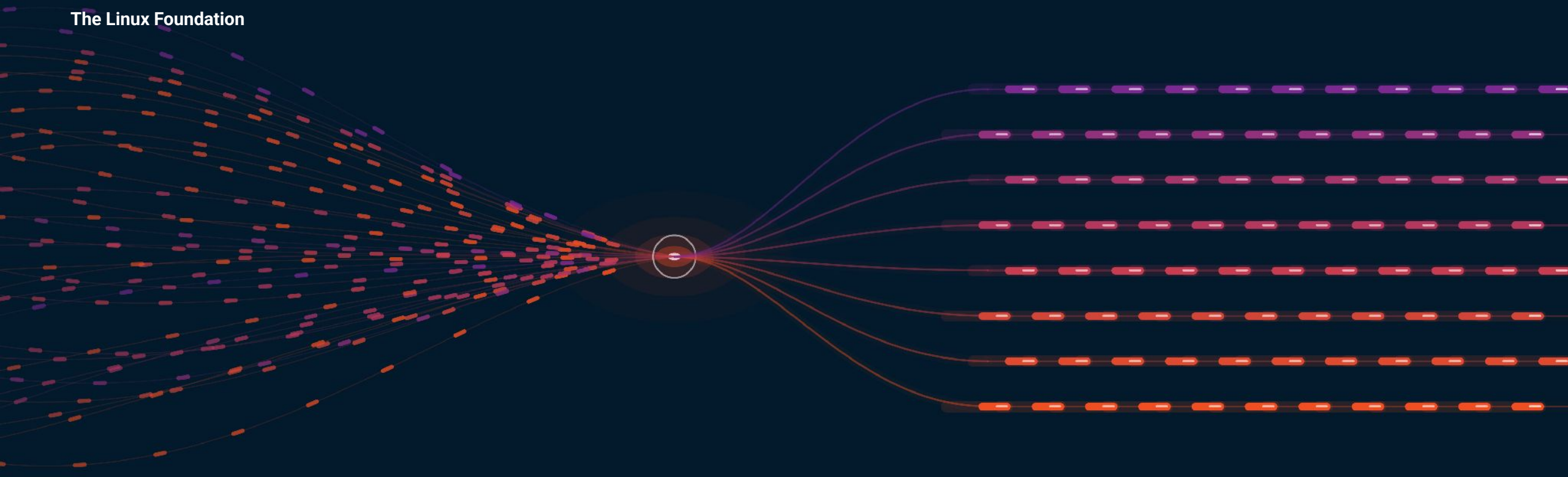


2027

Corporate Membership

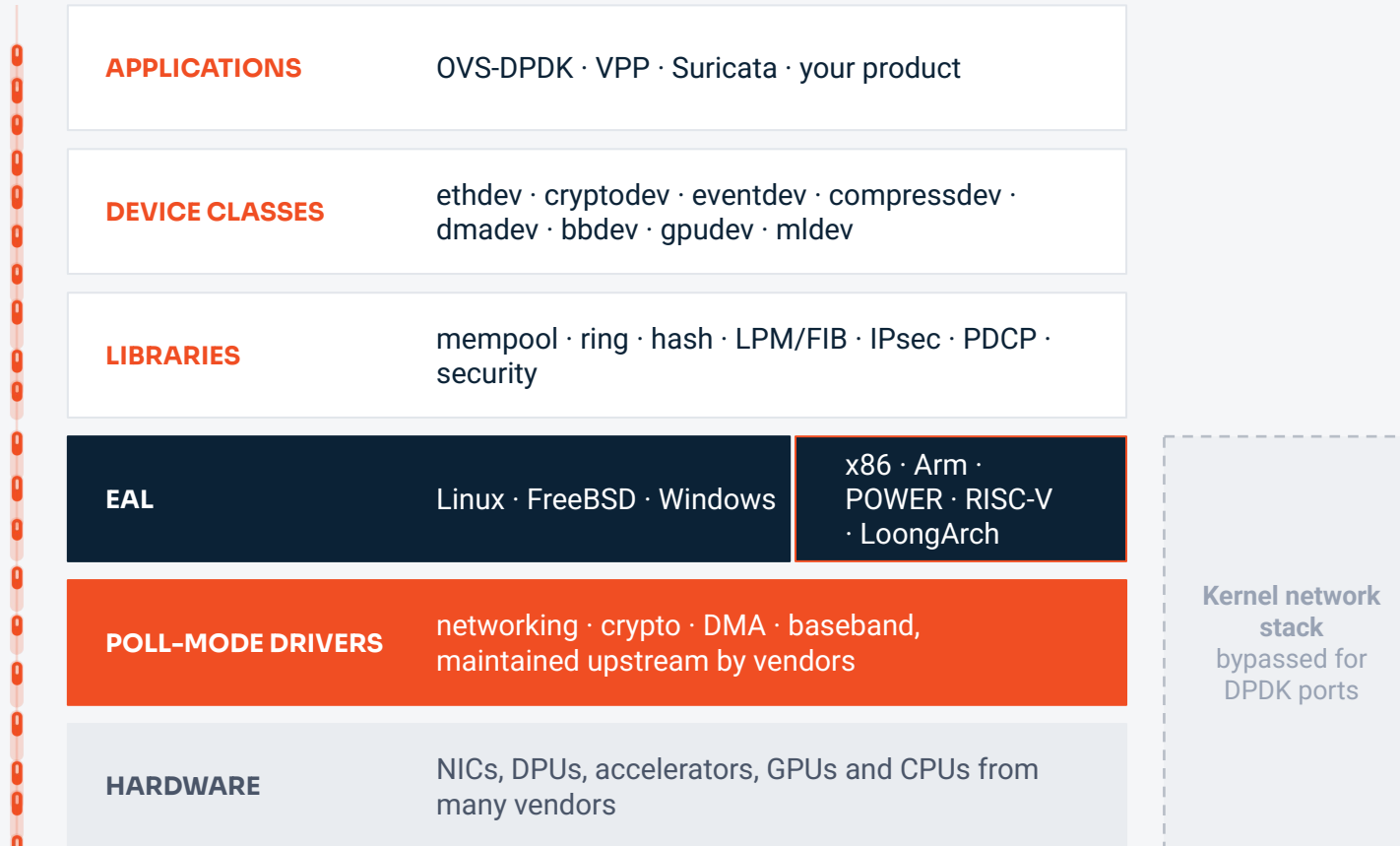
For current members · October 2026

The Linux Foundation



01 - THE PROJECT

DPDK moves packets in user space, on any hardware



Kernel bypass

Poll-mode drivers move packets between NIC DMA rings and user memory: no per-packet system calls or context switches on the fast path.

Memory model

Hugepages, lockless rings and mempools with per-core caches. Cache-aligned and NUMA-aware.

One API per device class

Applications query capabilities at runtime instead of coding to one vendor's hardware.

Open and portable

BSD-3-Clause licensed. Linux, FreeBSD and Windows on five CPU architectures.

01 • WHO FUNDS DPDK

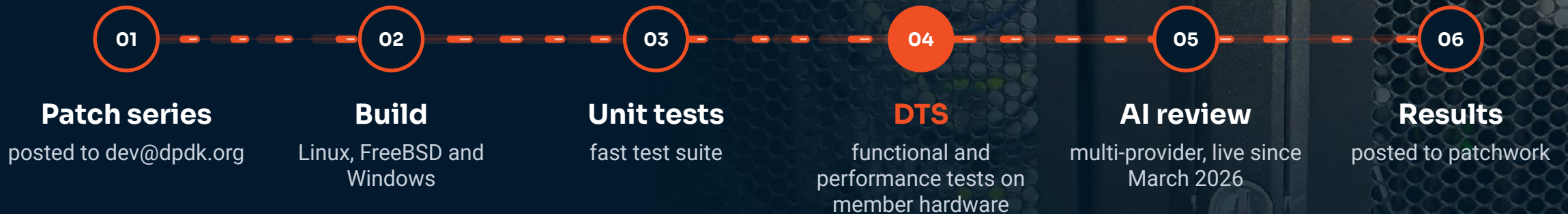
100% corporate member retention, 2023–2026

13 corporate members and 3 academic and research associates fund the project's shared infrastructure.

GOLD	        
SILVER	   
ASSOCIATE	   <i>Learning with Purpose</i>

Every patch, tested on real hardware.

Membership dues fund per-patch CI on member-provided hardware, so regressions on your devices surface upstream, not in your customers' deployments.



Lab leads: Lincoln Lavoie, UNH-IOL · Aaron Conole, Red Hat, DPDK Testing Leader

Lab hardware: AMD Genoa · NVIDIA Grace (Arm) · Intel Sapphire Rapids · Intel E810 · NVIDIA ConnectX-6/7 · Marvell CN10K · Intel QAT

03 - DUES AT WORK

Dues fund the lab and the people who run the project

01

Community Lab at UNH-IOL

Lab engineering, hardware, software licenses and space

- AI code review in per-patch CI, live since March
- Cryptodev tests migrated to DTS
- Fedora 43, RHEL 10 and Windows Server 2025 builds
- Per-patch testing for nearly 10,000 patches in 2025

02

Linux Foundation project services

Events, program management and marketing

Events

DPDK Summit Stockholm, May 2026: 29 recorded sessions

Program management

Governing Board and Technical Board operations and elections

Marketing and DevRel

The Dispatch, spotlights, user stories, APAC outreach

03 - DUES AT WORK

Your dues added AI review to every patch

DPDK received nearly 10,000 patches in 2025. In 2026 the Governing Board funded three changes to how they are tested.

LIVE SINCE MARCH 2026

AI code review in per-patch CI

Every series that applies cleanly, builds and passes the fast tests gets an automated review in its patchwork checks. The multi-provider review scripts and AGENTS.md are in the tree as of 26.07, so contributors can run the same review before submitting.

MIGRATED

Cryptodev tests in DTS

Remaining legacy testing, including cryptodev, moved to the current DTS framework, starting with Intel QAT.

ADDED

New build platforms

Fedora 43, RHEL 10 and Windows Server 2025 added to the per-patch build matrix.

04 • THE PROOF

~25%

The largest single company's share of commits across 25.11, 26.03 and 26.07. No one vendor controls the codebase it depends on.



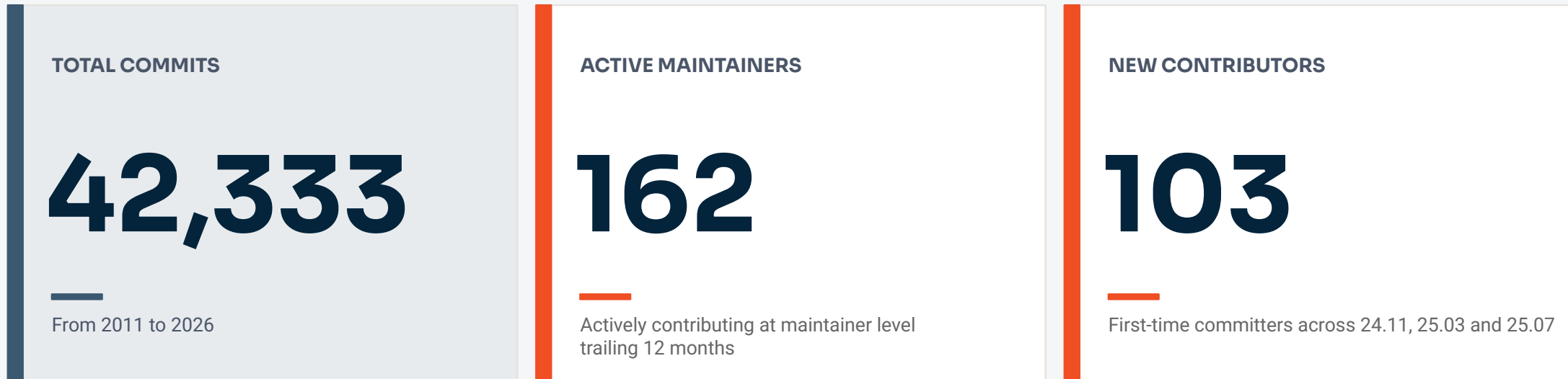
45,000+ commits since 2012 **2,601** in three releases **300** active contributors **~100** first-time

Source: DPDK git history. Three releases: 25.11, 26.03, 26.07.

04 • WHO CONTRIBUTES

Track record of stability & growth

A decade-plus of multi-vendor contribution, a stable maintainer base, and continued intake of first-time contributors.



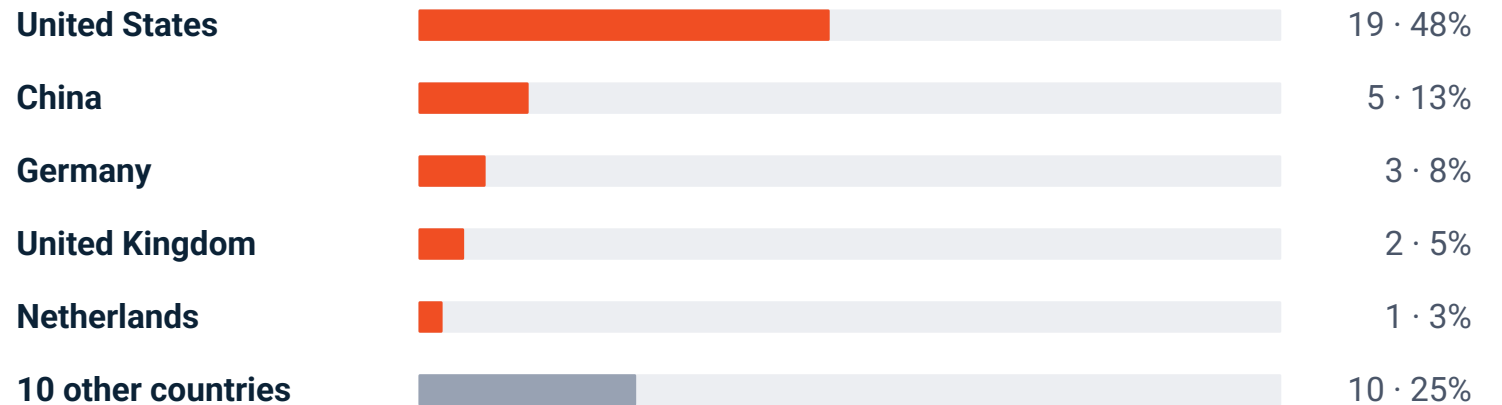
04 • WHO CONTRIBUTES

Contributors in 15 countries; new drivers from APAC

ACTIVE ORGANIZATIONS

40

12 months to September 2026, in 15 countries



GROWTH IN CHINA, SINGAPORE AND INDIA

4 of 5

new NIC and DMA drivers since 25.11: Huawei hinic3 and HiSilicon DMA, Nebulamatrix nbl, LinkData sxe2

1:1

14K engaged sessions on dpdk.org from China and the US

July 2026

APAC Outreach Committee formed; WeChat and bilibili channels launched

“

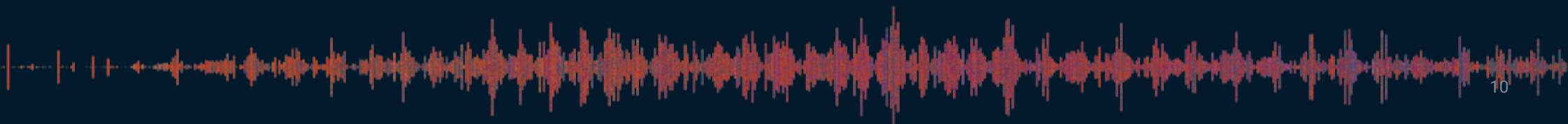
DPDK is not a one-company or two-company project.

There are so many people from different companies, academia, and even small organizations who contribute. It's flexible, works across many kinds of hardware without vendor lock-in, and provides predictable, ultra-low latency packet processing.



Rashid Khan

Senior Director CPU, GPU, Edge, Networking, Red Hat · Former DPDK Governing Board Chair



The same drivers carry physics and astronomy data



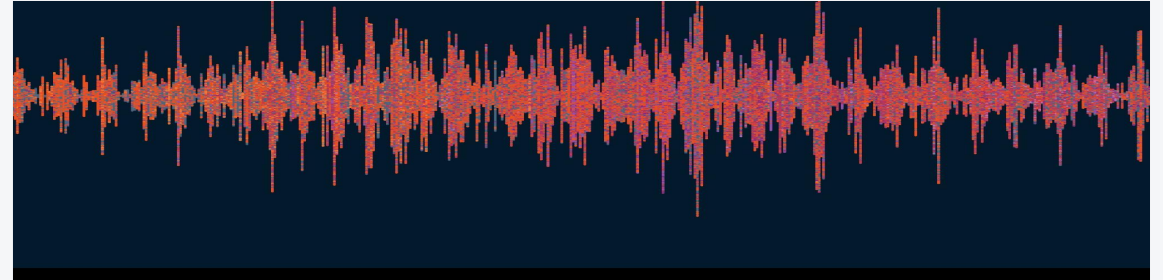
HIGH-ENERGY PHYSICS • CERN, GENEVA

400 Gbps

per node for DUNE prototypes

DPDK has run across CERN experiments for seven years, starting with NA62. DUNE work scales to 15 Tbit/s per supermodule across roughly 40 nodes. LHCb R&D has shown near line-rate 400G UDP reception on a single thread, under evaluation for HL-LHC upgrade DAQ.

Roland Sipos, CERN • DPDK Summit Stockholm, May 2026



RADIO ASTRONOMY • CHIME, BRITISH COLUMBIA

6.4 Tb/s

of UDP from custom FPGA boards

Andre Renard's team uses DPDK poll-mode drivers with Intel DDIO to inspect headers in L3 cache and place payloads at GPU-ready offsets in DRAM, halving memory operations per byte. The Kotekan pipeline built on this runs at multiple telescope sites.

Andre Renard, University of Toronto / CHIME • dpdk.org user story, March 2026

05 · RECENT RELEASES

New in 2026: RISC-V vectors, 5G crypto and PTP

ETHDEV

26.07

Selective Rx

Discard parts of received packets by assigning no mempool to selected buffer-split segments.

RISC-V

26.07

RISC-V vector paths

Vectorized ACL, up to 8 flows in parallel, and vectorized IPv4 LPM lookup in the node library.

CRYPTODEV

26.03

256-bit wireless algorithms

NEA, NIA and NCA modes for SNOW 5G, AES-256 and ZUC-256.

VHOST

26.07

Dynamic memory regions

Add and remove guest memory regions without resetting the guest memory map.

NET

26.07

PTP support

IEEE 1588 definitions plus an example software transparent-clock relay.

BPF

26.07

Hardened BPF validator

Bounds-tracking and undefined-behavior fixes, plus a validator debugging API.

Driver updates for hardware from AMD, CESNET, Google, Huawei, Intel, LinkData, Marvell, Microsoft, NVIDIA, NXP, Wangxun and ZTE.

Source: DPDK 26.03 and 26.07 release notes.

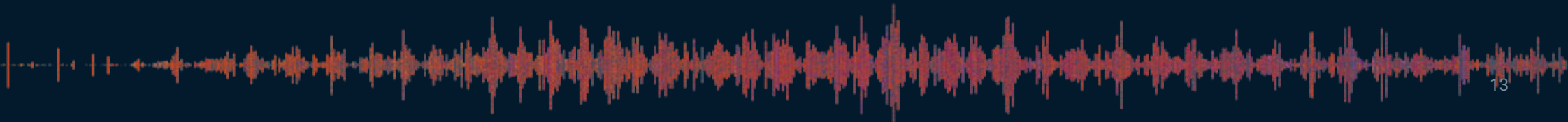
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We need solutions that can scale across a wide range of platforms without the fine-tuning we've done before. **DPDK's abstraction layers are crucial for this.** Using open source brings the benefit of community contributions, allowing us to support more hardware as different vendors develop new drivers.



Niklas Widell

Standardization Manager, Ericsson · DPDK Governing Board



05 · VISIBILITY

Your engineers' work, published and promoted

To request a user story or Dispatch feature, email marketing@dpdk.org.

VIDEO SERIES

DPDK Summit Stockholm 2026



29 recorded sessions, including CERN on DUNE data acquisition and 802.11 performance testing with iPerf.

Watch the series →

DEV SPOTLIGHT

Twelve Years of Building DPDK's Open Source Community

Thomas Monjalon, lead maintainer

INTERVIEW

Tim O'Driscoll: The View from the Governing Board

Intel · Governing Board Chair

USER STORY

How CHIME's Correlator Team uses DPDK to Turn Raw Sky into Science

University of Toronto / CHIME

USER STORY

NXP i.MX 95 in NVIDIA Holoscan Sensor Bridge

16.5 ms end-to-end for 4K camera data

USER STORY

Microsoft Azure MANA and DPDK

Microsoft · Gold member

NEWSLETTER

The DPDK Dispatch

Quarterly on LinkedIn · [Subscribe](#)

Once a year, in one room.

The rest of the year, the work happens on
the public mailing lists.

2025 Prague · 8–9 May

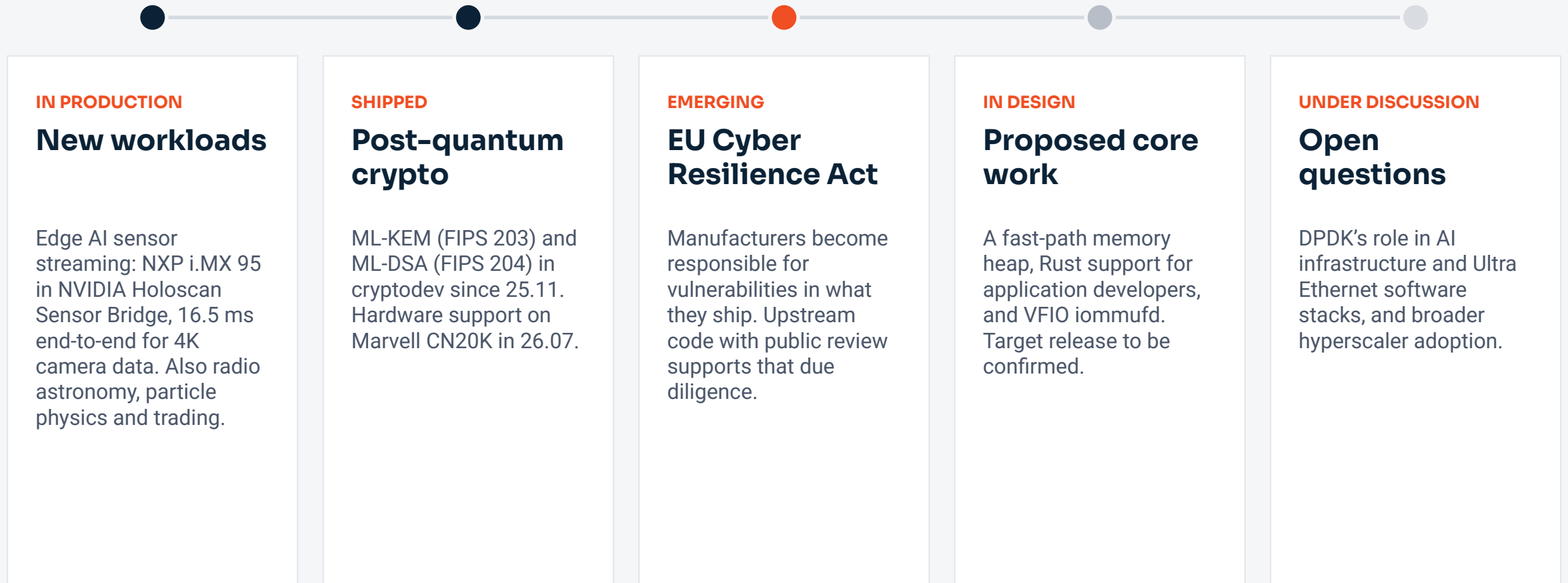
2026 Stockholm · 12–13 May

2027 To be announced

*Who attends (Prague 2025): 47% systems developers and
architects; 53% IT and 34% telecommunications.*

06 · WHAT'S NEXT

From shipped to still open: where DPDK is going



WHY JOIN

What you get depends on how you use DPDK

01

If you ship hardware

NIC, DPU, CPU and accelerator vendors

- Your devices in the Community Lab test matrix, exercised by per-patch CI.
- Your drivers maintained upstream, behind one API your customers already use.
- A Gold seat with a vote on lab priorities, budget and events.

02

If you build on DPDK

Operators, cloud providers, appliance vendors

- Upstream your changes instead of carrying out-of-tree patches across releases.
- SmartShare Systems, a user rather than a silicon vendor, has shaped APIs toward how applications consume them.
- A voice in which hardware and tests the lab funds.

03

For every member

- Neutral governance: budget, lab and trademark are held by the project, not one vendor.
- Upstream code with public review, increasingly relevant to EU Cyber Resilience Act due diligence.
- Visibility at the Summit, in the Dispatch and through user stories.

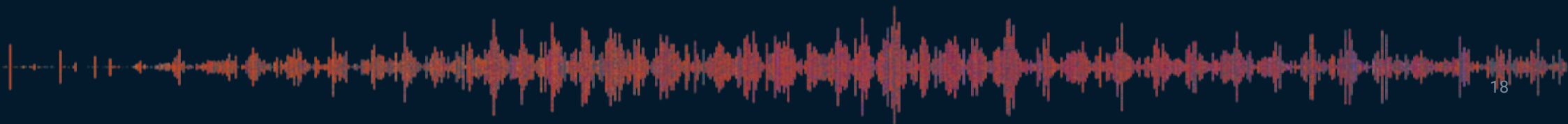
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It's all about **being on the inside setting the direction** rather than on the outside looking in, having to follow a direction set by others.



Tim O'Driscoll

Engineering Manager, Intel · DPDK Governing Board Chair



MEMBERSHIP

Moving to Gold adds a board seat and your hardware in the lab

Gold includes everything in Silver. To discuss moving to Gold: support@dptdk.org

	GOLD \$50,000 / year	SILVER \$5,000–\$25,000 (Based on company employee count)	ASSOCIATE No fee
Eligibility	Any organization	Any organization	Non-profit, academic, government
Linux Foundation membership	Required, any level	Required, any level	LF Associate membership
Governing Board	Dedicated seat; vote on budget, marketing, lab and new hosted projects	Elected seat: one per five Silver members, up to two	—
Governing Board work groups	✓	—	—
Community Lab testing on your hardware	✓	—	—
Customer referral for hardware validation	✓	—	—
DPDK and LF marketing visibility	✓	✓	—
Priority Summit sponsorship and events	✓	✓	Events and working sessions
Logo on dptdk.org	✓	✓	✓

Technical Board membership and commit rights are earned through contribution and are independent of membership tier.

Keep the infrastructure you depend on running.

Your 2026 dues paid for AI review in per-patch CI, the Stockholm Summit, and the lab that tests every patch series. Renewing keeps that work going in 2027.

Renew for 2027 →

Gold \$50,000 · Silver \$5,000 – \$25,000 · Associate no fee

Renewing members: support@dpdk.org

dpdk.org · A Linux Foundation project

